Selection process for the post of Assistant Professor Grade-II (Level 10)
Ref: Advt No. Estt. /2023/Faculty/3336/VII/2601 Date: January 23, 2026

The selection process for the post of Assistant Professor Grade-II (Level 10) is as follows:

Stage	Type of Examination	Time	Maximum Marks
Stage-I	**Multiple Choice-Based Screening Test** *(For screening the candidates for Departmental Presentation & Personal Interview)*	90 Minutes	150 Marks (75 questions)
Stage-II	**Teaching demo and Personal Interview** *(Shortlisted candidates based on stage-I Test shall be invited for Document Verification, Departmental Presentation in the department, and personal interview before the selection committee)*	The shortlisted candidate will be required to produce original certificates for verification. Teaching demo by the candidate will be of 10 minutes each duration on any of the UG level courses of the respective discipline using blackboard/whiteboard only. The candidate will be required to appear before Selection Committee for a personal interview.	

Question papers will be in the English language only. All questions of the written Examination will be Objective type. For every wrong answer, there will be a negative marking @ 1/4th mark for each wrong answer. Compensatory time for Persons with Benchmark Disabilities (Divyangjan) will be provided as per the extant orders of the Government of India.

(i) Stage-I: Multiple Choice Based Written Examination of 90-minutes duration (Maximum marks 150)

a. All the eligible candidates in respective branches will be required to appear for a written examination. Those candidates who will qualify in the Written Examination shall only be eligible to appear for a Departmental presentation and Personal Interview before Selection Committee. All the candidates are required to asses themselves before appearing in the written test about fulfilling his/her eligibility for the post applied for. The written examination shall be held at SVNIT, Surat on the dates to be specified by the institute.

b. No request for relaxation for submission of documents/ change of venue/ date of examination/ interview shall be considered under any circumstances. The schedule of the written examination, document verification, teaching demo, and personal interview shall be intimated in due course of time.

c. The Syllabus of the written examination for DoECE, DoEE, DoCSE and DoAI is attached herewith as APPENDIX. However, for DoCSE and DoAI, there will be a common written examination.

d. Based on the performance in the written examination (Stage-I) of the respective department, following number of candidates, in order of relative marks secured, will be shortlisted for Document Verification, Teaching demo, and Personal Interview before Selection Committee.

Serial No.	Name of the Department	Number of candidates to be shortlisted after the Stage-I Examination for Document Verification, Teaching Demo and Personal Interview					
		UR	OBC	SC	ST	EWS	Total
1	Computer Science & Engineering	13	08	04	02	03	**30**
2	Electronics Engineering	13	08	04	02	03	**30**
3	Artificial Intelligence	13	08	04	02	03	**30**
4	Electrical Engineering	13	08	04	02	03	**30**

The number of shortlisted candidates may differ if the total number of candidates appeared in the Written Examination are less in number or in case of a tie in the marks secured by the candidates in the written examination etc. **It is also resolved that candidates scoring 0 (zero) or negative (-ve) marks will not shortlisted from the written examination.**

e. The candidates are required to bring one set of self-attested copies of documents along with the originals including certificates for qualification, experience, NOC, particulars of pay drawn issued from the respective competent authority, category certificate, and proof for credit points claimed, etc., at the time of document verification. In case the candidate is unable to produce the required documents/certificates at the time of document verification, his/her candidature will not be considered.

(ii) Stage II: Departmental Presentation and Personal Interview:

Based on the Written Examination, the candidates will be shortlisted for a teaching demo which will be held in the respective Department. For the teaching demo, each candidate is required to teach any UG-level course of the respective discipline for 10 minutes using a blackboard/whiteboard only.

The Institute reserves the right to restrict the number of candidates to be called for teaching demos and/or Personal Interviews for a particular post. However, the final selection will be based on performance in the personal Interview only.

-sd/-
REGISTRAR I/C

APPENDIX

DETAILED SYLLABUS

DoCSE and DoAI

Section 1: Engineering Mathematics

Discrete Mathematics: Propositional and first order logic. Sets, relations, functions, partial orders and lattices. Monoids, Groups. Graphs: connectivity, matching, coloring. Combinatorics: counting, recurrence relations, generating functions.

Linear Algebra: Matrices, determinants, system of linear equations, eigenvalues and eigenvectors, LU decomposition.

Calculus: Limits, continuity and differentiability. Maxima and minima. Mean value theorem. Integration.

Probability and Statistics: Random variables. Uniform, normal, exponential, Poisson and binomial distributions. Mean, median, mode and standard deviation. Conditional probability and Bayes theorem.

Computer Science and Information Technology

Section 2: Digital Logic

Boolean algebra. Combinational and sequential circuits. Minimization. Number representations and computer arithmetic (fixed and floating point).

Section 3: Computer Organization and Architecture

Machine instructions and addressing modes. ALU, data-path and control unit. Instruction pipelining, pipeline hazards. Memory hierarchy: cache, main memory and secondary storage; I/O interface (interrupt and DMA mode).

Section 4: Programming and Data Structures

Programming in C. Recursion. Arrays, stacks, queues, linked lists, trees, binary search trees, binary heaps, graphs.

Section 5: Algorithms

Searching, sorting, hashing. Asymptotic worst case time and space complexity. Algorithm design techniques: greedy, dynamic programming and divide-and-conquer. Graph traversals, minimum spanning trees, shortest paths

Section 6: Theory of Computation

Regular expressions and finite automata. Context-free grammars and push-down automata. Regular and contex-free languages, pumping lemma. Turing machines and undecidability.

Section 7: Compiler Design

Lexical analysis, parsing, syntax-directed translation. Runtime environments. Intermediate code generation. Local optimisation, Data flow analyses: constant propagation, liveness analysis, common subexpression elimination.

Section 8: Operating System

System calls, processes, threads, inter-process communication, concurrency and synchronization. Deadlock. CPU and I/O scheduling. Memory management and virtual memory. File systems.

Section 9: Databases

ER-model. Relational model: relational algebra, tuple calculus, SQL. Integrity constraints, normal forms. File organization, indexing (e.g., B and B+ trees). Transactions and concurrency control.

Section 10: Computer Networks

Concept of layering: OSI and TCP/IP Protocol Stacks; Basics of packet, circuit and virtual circuit-switching; Data link layer: framing, error detection, Medium Access Control, Ethernet bridging; Routing protocols: shortest path, flooding, distance vector and link state routing; Fragmentation and IP addressing, IPv4, CIDR notation, Basics of IP support protocols (ARP, DHCP, ICMP), Network Address Translation (NAT); Transport layer: flow control and congestion control, UDP, TCP, sockets; Application layer protocols: DNS, SMTP, HTTP, FTP, Email.

DoEE

Section 1: Engineering Mathematics

Linear Algebra: Matrix Algebra, Systems of linear equations, Eigenvalues, Eigenvectors.

Calculus: Mean value theorems, Theorems of integral calculus, Evaluation of definite and improper integrals, Partial Derivatives, Maxima and minima, Multiple integrals, Fourier series, Vector identities, Directional derivatives, Line integral, Surface integral, Volume integral, Stokes's theorem, Gauss's theorem, Divergence theorem, Green's theorem.

Differential Equations: First order equations (linear and nonlinear), Higher order linear differential equations with constant coefficients, Method of variation of parameters, Cauchy's equation, Euler's equation, Initial and boundary value problems, Partial Differential Equations, Method of separation of variables.

Complex Variables: Analytic functions, Cauchy's integral theorem, Cauchy's integral formula, Taylor series, Laurent series, Residue theorem, Solution integrals.

Probability and Statistics: Sampling theorems, Conditional probability, Mean, Median, Mode, Standard Deviation, Random variables, Discrete and Continuous distributions, Poisson distribution, Normal distribution, Binomial distribution, Correlation analysis, Regression analysis.

Section 2: Electric circuits

Network Elements: ideal voltage and current sources, dependent sources, R, L, C, M elements; Network solution methods: KCL, KVL, Node and Mesh analysis; Network Theorems: Thevenin's, Norton's, Superposition and Maximum Power Transfer theorem; Transient response of dc and ac networks, sinusoidal steady-state analysis, resonance, two port networks, balanced three phase circuits, star-delta transformation, complex power and power factor in ac circuits.

Section 3: Electromagnetic Fields

Coulomb's Law, Electric Field Intensity, Electric Flux Density, Gauss's Law, Divergence, Electric field and potential due to point, line, plane and spherical charge distributions, Effect of dielectric medium, Capacitance of simple configurations, Biot-Savart's law, Ampere's law,Curl, Faraday's law, Lorentz force, Inductance, Magnetomotive force, Reluctance, Magnetic circuits, Self and Mutual inductance of simple configurations.

Section 4: Signals and Systems

Representation of continuous and discrete time signals, shifting and scaling properties, linear time invariant and causal systems, Fourier series representation of continuous and discrete time periodic signals, sampling theorem, Applications of Fourier Transform for continuous and discrete time signals, Laplace Transform and Z transform. R.M.S. value, average value calculation for any general periodic waveform.

Section 5: Electrical Machines

Auto-transformer, Electromechanical energy conversion principles; DC machines: separately excited, series and shunt, motoring and generating mode of operation and their characteristics, speed control of dc motors; Three-phase induction machines: principle of operation, types, performance, torque-speed characteristics, no-load and blocked-rotor tests, equivalent circuit, starting and speed control; Operating principle of single-phase induction motors; Synchronous machines: cylindrical and salient pole machines, performance and characteristics, regulation and parallel operation of generators, starting of synchronous motors; Types of losses and efficiency calculations of electric machines.

Section 6: Power Systems

Basic concepts of electrical power generation, ac and dc transmission concepts, Models and performance of transmission lines and cables, Economic Load Dispatch (with and without considering transmission losses), Series and shunt compensation, Electric field distribution and insulators, Distribution systems, Per-unit quantities, Bus admittance matrix, Gauss- Seidel and Newton-Raphson load flow methods, Voltage and Frequency control, Power factor correction, Symmetrical components, Symmetrical and unsymmetrical fault analysis, Principles of over-current, differential, directional and distance protection; Circuit breakers, System stability concepts, Equal area criterion.

Section 7: Control Systems

Mathematical modelling and representation of systems, Feedback principle, transfer function, Block diagrams and Signal flow graphs, Transient and Steady-state analysis of linear time invariant systems, Stability analysis using Routh-Hurwitz and Nyquist criteria, Bode plots, Root loci, Lag, Lead and Lead-Lag compensators; P, PI and PID controllers; State space model, Solution of state equations of LTI systems

Section 8: Electrical and Electronic Measurements

Bridges and Potentiometers, Measurement of voltage, current, power, energy and power factor; Instrument transformers, Digital voltmeters and multi-meters, Phase, Time and Frequency measurement; Oscilloscopes, Error analysis.

Section 9: Analog and Digital Electronics

Simple diode circuits: clipping, clamping, rectifiers; Amplifiers: biasing, equivalent circuit and frequency response; oscillators and feedback amplifiers; operational amplifiers: characteristics and applications; single stage active filters, Active Filters: Sallen Key, Butterwoth, VCOs and timers, combinatorial and sequential logic circuits, multiplexers, demultiplexers, Schmitt triggers, sample and hold circuits, A/D and D/A converters.

Section 10: Power Electronics

Static V-I characteristics and firing/gating circuits for Thyristor, MOSFET, IGBT; DC to DC conversion: Buck, Boost and Buck-Boost Converters; Single and three-phase configuration of uncontrolled rectifiers; Voltage and Current commutated Thyristor based converters; Bidirectional ac to dc voltage source converters; Magnitude and Phase of line current harmonics for uncontrolled and thyristor based converters; Power factor and Distortion Factor of ac to dc converters; Single-phase and three-phase voltage and current source inverters, sinusoidal pulse width modulation.

DoECE

Section 1: Engineering Mathematics

Linear Algebra: Vector space, basis, linear dependence and independence, matrix algebra, Eigen values and eigen vectors, rank, solution of linear equations- existence and uniqueness.

Calculus: Mean value theorems, theorems of integral calculus, evaluation of definite and improper integrals, partial derivatives, maxima and minima, multiple integrals, line, surface and volume integrals, Taylor series.

Differential Equations: First order equations (linear and nonlinear), higher order linear differential equations, Cauchy's and Euler's equations, methods of solution using variation of parameters, complementary function and particular integral, partial differential equations, variable separable method, initial and boundary value problems.

Vector Analysis: Vectors in plane and space, vector operations, gradient, divergence and curl, Gauss's, Green's and Stokes' theorems.

Complex Analysis: Analytic functions, Cauchy's integral theorem, Cauchy's integral formula, sequences, series, convergence tests, Taylor and Laurent series, residue theorem.

Probability and Statistics: Mean, median, mode, standard deviation, combinatorial probability, probability distributions, binomial distribution, Poisson distribution, exponential distribution, normal distribution, joint and conditional probability.

Section 2: Networks, Signals and Systems

Circuit Analysis: Node and mesh analysis, superposition, Thevenin's theorem, Norton's theorem, reciprocity. Sinusoidal steady state analysis: phasors, complex power, maximum power transfer. Time and frequency domain analysis of linear circuits: RL, RC and RLC circuits, solution of network equations using Laplace transform.

Linear 2-port network parameters, wye-delta transformation.

Continuous-time Signals: Fourier series and Fourier transform, sampling theorem and applications.

Discrete-time Signals: DTFT, DFT, z-transform, discrete-time processing of continuous-time signals. LTI systems: definition and properties, causality, stability, impulse response, convolution, poles and zeroes, frequency response, group delay, phase delay.

Section 3: Electronic Devices

Energy bands in intrinsic and extrinsic semiconductors, equilibrium carrier concentration, direct and indirect band-gap semiconductors.

Carrier Transport: diffusion current, drift current, mobility and resistivity, generation and recombination of carriers, Poisson and continuity equations.

P-N junction, Zener diode, BJT, MOS capacitor, MOSFET, LED, photo diode and solar cell.

Section 4: Analog Circuits

Diode Circuits: clipping, clamping and rectifiers.

BJT and MOSFET Amplifiers: biasing, ac coupling, small signal analysis, frequency response. Current mirrors and differential amplifiers.

Op-amp Circuits: Amplifiers, summers, differentiators, integrators, active filters, Schmitt triggers and oscillators.

Section 5: Digital Circuits

Number Representations: binary, integer and floating-point- numbers. Combinatorial circuits: Boolean algebra, minimization of functions using Boolean identities and Karnaugh map, logic gates and their static CMOS implementations, arithmetic circuits, code converters, multiplexers, decoders.

Sequential Circuits: latches and flip-flops, counters, shift-registers, finite state machines, propagation delay, setup and hold time, critical path delay.
Data Converters: sample and hold circuits, ADCs and DACs. Semiconductor Memories: ROM, SRAM, DRAM.
Computer Organization: Machine instructions and addressing modes, ALU, data-path and control unit, instruction pipelining.

Section 6: Control Systems

Basic control system components; Feedback principle; Transfer function; Block diagram representation; Signal flow graph; Transient and steady-state analysis of LTI systems; Frequency response; Routh-Hurwitz and Nyquist stability criteria; Bode and root-locus plots; Lag, lead and lag-lead compensation; State variable model and solution of state equation of LTI systems.

Section 7: Communications

Random Processes: auto correlation and power spectral density, properties of white noise, filtering of random signals through LTI systems.

Analog Communications: amplitude modulation and demodulation, angle modulation and demodulation, spectra of AM and FM, super heterodyne receivers.

Information Theory: entropy, mutual information and channel capacity theorem.

Digital Communications: PCM, DPCM, digital modulation schemes (ASK, PSK, FSK, QAM), bandwidth, inter-symbol interference, MAP, ML detection, matched filter receiver, SNR and BER.

Fundamentals of error correction, Hamming codes, CRC.

Section 8: Electromagnetics

Maxwell's Equations: differential and integral forms and their interpretation, boundary conditions, wave equation, Poynting vector.

Plane Waves and Properties: reflection and refraction, polarization, phase and group velocity, propagation through various media, skin depth.

Transmission Lines: equations, characteristic impedance, impedance matching, impedance transformation, S-parameters, Smith chart.

Rectangular and circular waveguides, light propagation in optical fibers, dipole and monopole antennas, linear antenna arrays.